	CS401-ASSEMBLY LANGUAGE (Solved Macq's) LECTURE FROM (22 to 45)	
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1. Suppose AL contains 5 decimals then after two left shifts produces the value as

- 5
- 10
- 15
- 20 (Page 52)

2. In graphics mode a location in video memory corresponds to a_____on the screen.

- Line
- Dot (Page 149)
- Circle
- Rectangle

3. Creation of threads can be

- Static
- Dynamic (Page 141)
- Easy
- Difficult

4. The thread registration code initializes the PCB and adds it to the linked list so that the _____will give it a turn.

- Assembler
- Scheduler (Page 141)
- linked
- Debugger

5. VESA VBE 2.0 is a standard for

- High resolution Mode (Page 180)
- Low resolution Mode
- Medium resolution Mode
- Very High Resolution Mode

6. Which of the following gives the more logical view of the storage medium

- BIOS
- DOS (Page 155)
- Both
- None

7. Which of the following IRQs is derived by a key board?

- IRQ 0
- IRQ 1 (Page 114)
- IRQ 2

- IRQ 3

8. Which of the following IRQs is used for Floppy disk drive?

- IRQ 4
- IRQ 5
- IRQ 6 (Page 114)
- IRQ 7

9. Which of the following pins of a parallel port connector are grounded?

- 10-18
- 18-25 (Page 125)
- 25-32
- 32-39

10. The physical address of IDT (Interrupt Descriptor Table) is stored in _____

- GDTR
- IDTR (Page 182)
- IVT
- IDTT

11. In NASM an imported symbol is declared with the ..... while and exported symbol is declared
With the.....

- Global directive, External directive
- External directive, Global directive (Page 189)
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

12. In 68K processors there is a ..... program counter (PC) that holds the address of currently executing
Instruction

- 8bit
- 16bit
- 32bit (Page 191)
- 64bit

13. To reserve 8-bits in memory ___directive is used.

- db. (Page 25)
- dw
- dn
- dd

14. In the "mov ax, 5" 5 is the_____operand.

- Source (Page 18)
- Destination
- Memory

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- Register

15. RETF will pop the segment address in the

- CS register (Page 72)
- DS register
- SS register
- ES register

16. For the execution of the instruction "DIV BL", the implied dividend will be stored in

- AX (Page 85)
- BX
- CX
- DX

17. When a number is divided by zero "A Division by 0" interrupt is generated. Which instruction is used for this?

Purpose

- INT 0
- INT 1
- INT 2
- This interrupt is generated automatically (Page 107)

18. INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____

Register.

- AL (Page 152)
- BL
- CL
- BH

19. BIOS sees the disks as

- Logical storage
- Raw storage (Page 155)
- In the form of sectors only
- In the form of tracks only

20. In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?

- 1 (Page 171)
- 2
- 3
- 4

21. In 9pin DB 9, Signal ground is assigned on pin number

- 4
- 5 (Page 171)
- 6
- 3

22. In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ► 6
- ► 7

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- ► 8
- ► 9 (Page 171)

23. Motorola 68K processors have .....23bit general purpose registers.

- ► 4
- ► 8
- ► 16 (Page 191)
- ► 32

24. When two devices in the system want to use the same IRQ line then what will happen?

- ► An IRQ Collision
- ► An IRQ Conflict (Page 114)
- ► An IRQ Crash
- ► An IRQ Blockage

25. in the instruction MOV AX, 5 the number of operands are

- ► 1
- ► 2 (Page 25)
- ► 3
- ► 4

26. Which flags are NOT used for mathematical operations?

- ► Carry, Interrupt and Trap flag.
- ► Direction, Interrupt and Trap flag. (Page 133)
- ► Direction, Overflow and Trap flag.
- ► Direction, Interrupt and Sign flag

27. The physical address of the stack is obtained by

- ► SS: SI combination
- ► SS: SP combination (Page 68)
- ► ES: BP combination
- ► ES: SP combination

28. Value of AH in the write Graphics pixel service is

- ► 0Ch (Page 152)
- ► 0Bh
- ► 1Ch
- ► 2Ch

29. Threads can have function calls, parameters and _____variables.

- ► Global
- ► Local (Page 141)
- ► Legal
- ► Illegal

30. Creation of threads can be

- ► Static
- ► Dynamic (Page 141) rep
- ► Easy

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- ► Difficult

31. How many prevalent calling conventions do exist?

- ► 1
- ► 2 (Page 187)
- ► 3
- ► 4

32. VESA VBE 2.0 is a standard for

- ► High resolution Mode (Page 180) rep
- ► Low resolution Mode
- ► Medium resolution Mode
- ► Very High resolution Mode

33. The serial port connection is a ---------- connector

- ► 9pin DB 9 (Page 171)
- ► 8pin DB 9
- ► 3pin DB 9
- ► 9pin DB 5

34. Which of the following gives the more logical view of the storage medium

- ► BIOS
- ► DOS (Page 55) rep
- ► Both
- ► None

35. In STOSB instruction, when DF is clear, SI is

- ► Incremented by 1 (Page 92)
- ► Incremented by 2
- ► Decremented by 1
- ► Decremented by 2

36. After the execution of STOSW the CX will be

- ► Decremented by 1 (Page 92)
- ► Decremented by 2
- ► Incremented by 1
- ► Incremented by 2

37. IRQ is referred to

- ► Eight input signals
- ► One output signal
- ► One input signals
- ► Eight output signals

38. Which of the following IRQs is derived by a key board?

- ► IRQ 0
- ► IRQ 1 (Page 113) rep
- ► IRQ 2
- ► IRQ 3

39. Which of the following IRQs is connected to serial port COM 1?

- ► IRQ 4 (page 114)
- ► IRQ 5
- ► IRQ 6

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- ► IRQ 7

40. The physical address of IDT (Interrupt Descriptor Table) is stored in _____

- ► GDTR
- ► IDTR (Page 182) rep
- ► IVT
- ► IDTT

41. Assembly language is:

- ► Low-level programming language (Page 3)
- ► High-level programming language
- ► Also known as machine language
- ► not considered closer to the computer

42. The number of bits required to access 1MB of memory are

- ► 16 bits
- ► 32 bits
- ► depends on the processor architecture (Page 20)
- ► 20 bits

43. In STOSB instruction, SI is decremented or incremented by

- ► 3
- ► 2
- ► 1 (Page 92)
- ► 4

44. In programmable interrupt controller, which of the following ports is referred as a control port.

- ► 19
- ► 20 (Page 115)
- ► 21
- ► 22

45. INT 21 service 01H is used to read character from standard input with echo. It returns the result In_____register.

- ► AL (Page 152) rep
- ► BL
- ► CL
- ► BH

46. In device attribute word, which of the following bit decides whether it is a character device or a block device

- ► Bit 12
- ► Bit 13
- ► Bit 14
- ► Bit 15 (Page 116)

47. In 9pin DB 9, which pin number is assigned to CTS (Clear to send)?

- ► 6
- ► 7
- ► 8 (Page 171)
- ► 9

48. In 9pin DB 9, which pin number is assigned to RD (Received Data)?

- ► 1
- ► 2 (Page 171)

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- ► 3
- ► 4

49. VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in

- 5:5:5 format
- ► 5:6:5 format (Page 180)
- ► 6:5:6 format
- ► 5:6:7 format

50. Motorola 68K processors have .....23bit general purpose registers.

- ► 4
- ► 8
- ► 16 (Page 191) rep
- ► 32

51. Programmable Interrupt Controller (PIC) has

- ► One input signals and eight output signals
- ► One input signal and one output signal
- ► Eight input signals and one output signals (Page 113)
- ► Eight input signals and eight output signals

52. Video services are classified into.....broad categories.

- ► 5
- ► 4
- ► 3
- ► 2 (Page 149

53. BL contains 5 decimal then after right shift, BL will become

- ➢ 3
- ➢ 2.5 (Page 52)
- ➢ 5
- ➢ 10

54. 8 * 16 font is stored in_____bytes.

- ➢ 3
- ➢ 4
- ➢ 8
- ➢ 16 (Page 150)

55. In DOS input buffer, number of characters actually read on return is stored in

- ➢ First byte
- ➢ Second byte (Page 152)
- ➢ Third byte
- ➢ Fourth byte

56. IRQ 0 has priority

- ➢ Low
- ➢ High
- ➢ Highest (Page 114)
- ➢ Medium

57. Thread registration code initialize PCB and add to linked list so that_____will give it turn.

- ➢ Assembler
- ➢ Linker

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- Scheduler (Page 141) rep
- Debugger

58... Traditional calling conventions are in_____number

- 1
- 2 (Page 187)
- 3
- 4

59. VESA VEB 2.0 is standard for

- High Resolution Mode (Page 180) rep
- Low Resolution Mode
- Very High Resolution Mode
- Medium Resolution Mode

60. to clear direction flag which instruction is used?

- Cld (Page 92)
- Clrd
- Cl df
- Clr df

61. In STOSW instruction, When DI is cleared, SI is

- Incremented by 1
- Incremented by 2 (Page 92)
- Decremented by 1
- Decremented by 2

62 Interrupt that is used in debugging with help of trap flag is

- INT 0
- INT 1 (Page 105)
- INT 2
- INT 3

63. INT for arithmetic overflow is

- INT 1
- INT 2
- INT 3
- INT 4 (Page 106)

64. IRQ referred as

- Eight Input signals (Page 113) rep
- One Input signal
- Eight Output signals
- One output signal

65. IRQ for keyboard is

____1_____(Page 114)

66. IRQ for sound card is

_____5_____(Page 114)

67. IRQ for floppy disk is

_____6_____(Page 114)

68. IRQ with highest priority is

- Keyboard IRQ

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- Timer IRQ (Page 114)
- Sound Card
- Floppy Disk

69. Pin for parallel port ground is

- 10-18
- 18-25 (Page 125) rep
- 25-32
- 32-39

70. The physical address of Interrupt Descriptor Table (IDT) is stored in

- GDTR
- IDTR (Page 182) rep
- IVT
- IDTT

71. CX register is

- Count register (Page 15)
- Data register
- Index register
- Base register

72. OUT instruction uses

__AX or AL_____as source register. (Page 115)

73. IN DB-9 connector the Data Set ready pin is at

- 5
- 6 (Page 171)
- 7
- 8

74. If two devices uses same IRQ then there is

- IRQ collision
- IRQ conflict (Page 114) rep
- IRQ drop

75. VESA organizes 16 bit color for every pixel in ratio

- 5:5:5
- 5:6:5 (Page 180) rep
- 6:5:6
- 5:6:

76. SP is associated with.....By default

- SS (Page 34)
- DS
- CS
- ES

77. Which bit of the attributes byte represents the red component of foreground color?

- 5
- 4
- 3
- 2 (Page 81)

78. An 8 x 16 font is stored in_____bytes.

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- 2
- 4
- 8
- 16 (Page 150) rep

79. In DOS input buffer, the number of characters actually read on return is stored in____byte.

- Third
- Fourth
- First
- Second (Page 152) rep

80. Which of the following gives the more logical view of the storage medium

- BIOS
- DOS
- Both
- None

81. In STOSW instruction, when DF is clear, SI is

- Incremented by 1
- Incremented by 2 (Page 92)
- Decremented by 1
- Decremented by 2

82. Which of the following interrupts is Non mask able interrupt

- INT 2 (Page 105)
- INT 3
- INT 0
- INT 1

83. Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3 (Page 114)

84. The time interval between two timer ticks is?

- 40ms
- 45ms
- 50ms
- 55ms (Page 122)

85. The physical address of IDT (Interrupt Descriptor Table) is stored in _____

- GDTR
- IDTR (Page 182) rep
- IVT
- IDTT

86. In NASM an imported symbol is declared with the ..... while and exported symbol is declared With the.....

- Global directive, External directive
- External directive, Global directive (Page 189) rep
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

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87. In 68K processors there is a 32bit.....that holds the address of currently executing instruction

- ► Program counter (Page 191)
- ► Stack pointer
- ► Register
- ► Stack

88. Single step interrupt is

- ► Hardware interrupt
- ► like divide by zero interrupt (Page 133)
- ► like divide by 1 interrupt
- ► Software interrupt

89. Which of the following is NOT true about registers:

- ► their operation is very much like memory
- ► Intermediate results may also be stored in registers
- ► They are also called scratch pad ram
- ► None of given options

90. Types of jump are:

- ► Short, near
- ► Short, near, far (Page 45)
- ► near, far
- ► short, far

91. MS DOS uses_____display mode.

- ► Character based (Page 79)
- ► Graphics based
- ► Numeric based
- ► Console based

92. Which of the following IRQs is derived by a timer device?

- ► IRQ 0 (Page 114)
- ► IRQ 1
- ► IRQ 2
- ► IRQ 3

93. In programmable interrupt controller, which of the following ports is referred as a control port?

- ► 19
- ► 20 (Page 115) rep
- ► 21
- ► 22

94. INT 21 service 01H is used to read character from standard input with echo. It returns the result in_____register.

- ► AL (Page 152) rep
- ► BL
- ► CL
- ► BH

95. In 9pin DB 9, which pin number is assigned to DSR (Dataset Ready)?

- ► 4
- ► 5
- ► 6 (Page 171)

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- 7

96. In 9pin DB 9, which pin number is assigned to TD (Transmitted Data)?

- 1
- 2
- 3 (Page 171)
- 4

97. In 9pin DB 9, Signal ground is assigned on pin number

- 4
- 5 (Page 171)
- 6
- 3

98.8088 is a.....

- 16 bit processor (Page 14)
- 32 bit processor
- 64 bit processor
- 128 bit processor

99. The table index (TI) is set to_____to access the GDT (Global Descriptor Table).

- 1
- 0 (Page 175)
- -1
- -2

100. VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in

- 5:5:5 format
- 5:6:5 format (Page 180) rep
- 6:5:6 format
- 5:6:7 format

101. Which flags are NOT used for mathematical operations?

- Carry, Interrupt and Trap flag.
- Direction, Interrupt and Trap flag. (Page 133) rep
- Direction, Overflow and Trap flag.
- Direction, Interrupt and Sign flag

102. When a 32 bit number is divided by a 16 bit number, the quotient is of

- 32 bits
- 16 bits (Page 85)
- 8 bits
- 4 bits

103. in the instruction MOV AX, 5 the number of operands are

- 1
- 2 (Page 25) rep
- 3
- 4

104. In DOS input buffer, number of characters actually read on return is stored in

- First byte
- Second byte (Page 152) rep
- Third byte
- Fourth byte

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105. VESA VEB 2.0 is standard for
- High Resolution Mode (Page 180) rep
- Low Resolution Mode
- Very High Resolution Mode
- Medium Resolution Mode

106. IN DB-9 connector the Data Set ready pin is at
- 5
- 6 (Page 171) rep
- 7
- 8

107. Threads can have function calls, parameters and variables.
- Global
- Local (Page 141)
- Legal
- Illegal

108. How many prevalent calling conventions do exist?
- 1
- 2 (Page 187) rep
- 3
- 4

109. In 9pin DB 9 DSR is assigned on pin number
- 4
- 5
- 6 (Page 171)
- 7

110. In 9pin DB 9 CTS is assigned on pin number
- 6
- 7
- 8 (Page 171)
- 9

112. In 9pin DB 9 CD is assigned on pin number
- 1 (Page 171)
- 2
- 3
- 4

113. A 32bit address register can access up to ...access has .of memory so memory increased a lot.
- 2GB
- 4GB (Page 175)
- 6GB
- 8GB

114. In device attribute word which of the following bit decides whether it is a character device or a block device
- Bit 12
- Bit 13
- Bit 14
- Bit 15 (Page 166) rep

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115. Which of the following IRQ is cascading interrupt

- IRQ 0
- IRQ 1
- IRQ 2 (Page 114)
- IRQ 3

116. Which of the following interrupts is used for Arithmetic overflow

- INT 1
- INT 2
- INT 3
- INT 4 (Page 106) rep

117. An End of Interrupt (EOI) signal is sent by

- Handler (Page 114)
- Processor
- IRQ
- PIC

118. The number of pins in a parallel port connector are?

- 20
- 25 (Page 125)
- 30
- 35

119. Which of the following pins of a parallel port connector are grounded?

- 10-18
- 18-25 (Page 125) rep
- 25-32
- 32-39

120. In NASM an imported symbol is declared with the ..... while and exported symbol is declared With the.....

- Global directive, External directive
- External directive, Global directive (Page 189) rep
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

121. Sun SPARC Processor has a fixed_____instruction size.

- 16bit
- 32bit (Page 192) rep
- 64bit
- 20bit

122. When the subprogram finishes, the_____retrieves the return address from the stack and transfers control to that location.

- RET instruction Click here for detail
- CALL instruction
- POP instruction
- Jump instruction

123. A 32 bit address register can access up to_____of memory.

- 1 GB
- 6 GB
- 4 GB (Page 175) rep
- 2 GB

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124. The value of a segment register when the processor is running under protected mode is called _____

- segment descriptor
- segment selector
- global descriptor table
- protected register

125. FS and FS and GS are two_____in protected mode.

- segment registers
- segment selectors (Page 175)
- stack pointers
- register pointers

126. IRQ 0 interrupt have_____priority

- low
- medium
- highest (Page 114) rep
- lowest

127. IDT stands for_____.

- interrupt descriptor table (Page 182)
- individual descriptor table
- inline data table
- interrupt descriptor table

128. Every bit of line status in serial port conveys_____information.

- different (Page 171)
- same
- partial
- full

129. There are total_____bytes in a standard floppy disk.

- 1444k
- 1440k (Page 155)
- 1280k
- 2480k

130. An 8x16 font is stored in_____bytes.

- 8
- 16 (Page 150) rep
- 4
- 20

131. Memory address space is selected when which of the following instructions is given to the processors Select correct option:

- MOV (page 115)
- DEC
- IN
- ADD

132. PCB stands for?

- Select correct option:
- Process Control Block (Page 140)
- Process Clearing Block
- Programmable Counter Block

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- Programs Control Block

132. The input frequency of the programmable interval timer (PIT) is

- Fixed (Page 122)
- Depends on processor clock
- Variable
- Depends on hardware attached

133. Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is.....

- The Interrupt mask register (Page 115)
- Interrupt port
- Output port
- Input port

134. The time interval between two timer ticks is?

- 40ms
- 45ms
- 50ms
- 55ms (Page 122)

135. Which of the following interrupts is used for saving and restoring the registers

- INT6
- Int7
- Int8 (Page 141)
- Int0

136. In programmable interrupt controller which of the following ports is referred as an interrupt mask register?

- 19
- 20
- 21 (Page 115)
- 22

137. The programmable interval timer (PIT) has input frequency of

- 193MHZ (Page 122)
- 192MHZ
- 196MHZ
- 198MHZ

138. Peripheral address space is selected when which of the following instructions is given to the processor?

- MOV
- IN (Page 155)
- Add
- Out

139. Direction flag, the interrupt flag, and the trap flag are

- used for mathematical operations
- not used for mathematical operations (Page 133)
- status flags
- not status flags

140. The space where all the registers of a task are stored is called the

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- control block (Page 140)
- process control block
- stack
- memory

141. Which of the following interrupt is of highest priority interrupt?

- Key board interrupt
- Timer interrupt (Page 122)
- INT 2
- INT 3

142. The instruction used to read a character from the keyboard port is

- in al, 0x60 (Page 125)
- out al, 0x60
- in al, 0x80
- out al, 0x80

143. Each thread can have their own

- execution area
- stack (Page 141)
- memory
- array

144. All the registers & stack are saved in

- Multitasking
- multi-processing
- function call
- BIOS

145. There is no instruction to clear the

- Trap flag (Page 133)
- Interrupt flag
- Direction flag
- None of the above

146. The interrupt mask register which can be used for selectively enabling or disabling interrupts is associated with

- Port 19
- Port 20
- Port 21 (Page 115)
- Port 22

147. The parallel port connector is called?

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- DB-25 (Page 125)
- BD-25
- DB-24
- BD-24

148. Which of the following is not true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers.
- They are also called scratch pad ram
- None of given options.

149. move [bop], al moves the one byte content of the AL register to the address contained in

- BP register in the current
- Stack segment (Page 35)
- Code segment
- Data segment
- Extra segment

150. In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

- 16 bits rotation
- 1 bit rotation
- 17 bits rotation (Page 53)
- 8 bits rotation

151. The 8088 stack works on

- Word sized elements (Page 68)
- Byte sized elements
- Double sized element
- Nibble sized element

152. An 8 x 16 font is stored in_____bytes.

- 4
- 8
- 16 (Page 150) rep

153. INT 10 is used for_____services.

- RAM
- Disk
- BIOS video (Page 149)
- DOS video

154. Priority of IRQ 0 interrupt is

- medium
- high

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- highest (Page 114) rep
- low

155. Threads can have function calls, parameters and _____variables.

- global
- local (Page 141) rep
- legal
- illegal

156. How many prevalent calling conventions do exist?

- 1
- 2 (Page 187) rep
- 3
- 4

157. In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6 (Page 171) rep
- 7

158. In 9pin DB 9 CTS is assigned on pin number

- 6
- 7
- 8 (Page 171) rep
- 9

159. In 9pin DB 9 CD is assigned on pin number

- 1 (Page 171) rep
- 2
- 3
- 4

160. In 9pin DB 9 RD is assigned on pin number

- 1
- 2 (Page 171)
- 3
- 4

161. In device attribute word which of the following bit decides whether it is a character device or a block device

- Bit 12
- Bit 13

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- Bit 14
- Bit 15 (Page 166)

162. Video services are classified into_____broad categories

- 2 (Page 149)
- 3
- 4
- 5

163. In STOSB instruction, when DF is clear, SI is

- Incremented by 1 (Page 92) rep
- Incremented by 2
- Decremented by 1
- Decremented by 2

164. The process of sending signals back and forth is called

- Activity
- Hand-shaking
- Interruption
- Time clicking

165. Which of the following interrupts is used for Arithmetic Overflow?

- INT 1
- INT 2
- INT 3
- INT 4 (Page 106) rep

166. Which of the following is a special type of interrupt that returns to the same instruction instead of the next Instruction?

- Divide overflow interrupt (Page 107)
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

167. Which of the following IRQs is derived by a timer device?

- IRQ 0 (Page 114) rep
- IRQ 1
- IRQ 2
- IRQ 3

168. Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1

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- IRQ 2
- IRQ 3 (Page 114) rep

169. An End of Interrupt (EOI) signal is sent by

- Handler (Page 114) rep
- Processor
- IRQ
- PIC

170. The source registers in OUT is

- AL or AX (Page 115) rep
- BL or BX
- CL or CX
- DL or DX

171. In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts

- 19
- 20
- 21 (Page 115)
- 22

172. The number of pins in a parallel port connector are?

- 20
- 25 (Page 125) rep
- 30
- 35

173. Which of the following pins of a parallel port connector are grounded?

- 10-18
- 18-25 (Page 125) rep
- 25-32
- 32-39

174. If the decimal number "35" is shifted by two bit to left, the new value will be

- 35
- 70
- 140 (00100011 = 35, 10001100=140)
- 17

175. A 32bit address register can access unto .....Of memory so memory access has increased a lot.

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- 2GB
- 4GB (Page 175) rep
- 6GB
- 8GB

176. In NASM an imported symbol is declared with the ..... while and exported symbol is declared With the.....

- Global directive, External directive
- External directive, Global directive (Page 189) rep
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

177. Single step interrupt is

- Hardware interrupt
- Like divide by zero interrupt (Page 133) rep
- Like divide by 1 interrupt
- Software interrupt

176. In programmable interrupt controller which of the following ports is referred as interrupt mask register?

a. 19
b. 20
c. 21
d. 22

177. _____is the highest priority interrupt in interrupt controller?

a. IRQ 0
b. IRQ 1
c. IRQ 2
d. IRQ 3

178. IRET returns on the basis of____and _____

a. CS, IP
b. DS, IP
c. CS, SS
d. IP,SP

170. If_is set, the after every instruction a type 1 interrupt will be automaticallygenerated.

e. Parity flag
b. Trap flag
c. Carry flag
d. Overflow flag

180.The interrupt handler uses_____instruction to return back to the caller.

a. IRET
b. RETI

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c. INTR
d. RET

179. Which of the following interrupt is used for Arithmetic overflow?

a. INT 1
b. INT 2
c. INT 3
d. INT 4

180. During the execution of INT instruction , some contents are pushed on to the stack , the orderof pushing then is

a. CS , IP and then FLAGS register
b. IP,CS and then FLAGS register
c. c. FLAGS register, CS and then IP
d. FLAGS register, IP and the CS

181. Each of the bits at port_____corresponding to one of the IRQ lines.

d. 18
e. 19
f. 20
d. 21

181. Which of the following pins of the parallel port connector are grounded?

a. 10-18
b. 18-25
c. 25-32
d. 32-39

182. Which of the following interrupts plays the most significant part during step debugging of a program?

a. INT 0
b. INT 1
c. INT 2
d. INT 3

183. Which of the following IRQs is connected to serial port COM 2?

a. IRQ 0
b. IRQ 1
c. IRQ 2
d. IRQ 3

184. Which of the following IRQs is connected to serialport COM 1?

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

185. Which of the following IRQs is used by the parallel port?

a. IRQ 4
b. IRQ 5
c. IRQ 6
d. IRQ 7

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186. **In programmable interrupt controller which of the following ports is used to selectivelyenabling or disabling interrupts?**

a. 19
b. 20
c. 21
d. 22

187.

The space where all the registers of task are stored is called _____

a. Control block
b. Process control block
c. Stack
d. Memory

188. **Which of the following is the destination register in IN instruction?**

a. AL or AX
b. BL or BX
c. CL or CX
d. DL or DX

189. **Which of the following interrupts is used for saving and restoring the registers**

a. INT 6
b. INT 7
c. INT 8
d. INT 0

190. **In multitasking environment, which of the following structure is used to maintain theordering of active PCBs?**

a. Array
b. Register
c. Linked List
d. Stack

191. **In multitasking which of the following interrupts is used as scheduler during contextswitching?**

a. INT 21
b. INT 16
c. INT 13
d. INT 8

192. **Which of the following are required for thread entry?**

a. CS and DS
b. CS and IP
c. IP and general purpose registers
d. SS and SP

193. **Which of the following flag can be used for mathematical operations?**

a. Direction Flag
b. Carry Flag
c. Trap Flag
d. Interrupt Flag

194.

In multi- tasking, the process of saving and restoring to values of registers from a

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processcontrol block (PCB) is called _

a. Context switching
b. Context saving
c. Context restoring
d. Code switching

195. When two devices in a system want to use the same IRQ line, is referred as:

a. IRQ Collision
b. IRQ Conflict
c. IRQ Crash
d. IRQ Blockage

196. An End of Interrupt (EOI) signal is sent by

a. Handler
b. Processor
c. IRQ
d. PIC

197. Which of the following is the BIOS interrupt providing keyboard service?

a. INT 0x13
b. INT 0x14
c. INT 0x15
d. INT 0x16

198.

Threads can have function calls, parameters and_____variables.

a. Global
b. Local
c. Legal
d. Illegal

199. Which of the following arranges jobs in a sequence in order to be executed?

a. Process control block
b. Arranger
c. Control unit
d. Scheduler

200. Which of the following flags cannot be cleared using an assembly instruction?

a. Trap flag
b. Interrupt flag
c. Direction flag
d. Carry flag

201. Which of the following is the highest priority interrupt?

a. INT3
b. INT2
c. INT1
d. INT0

202. At the end of servicing an interrupt, which of the following is used to inform tha PIC that it is completed?

a. RET
b. EOI
c. IRET
d. RET N

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203. Which of the following interrupts is used for maintaining the system time?

a. INT 0
b. INT 1
c. INT 8
d. INT 10

204. Which of the following is used for exporting parallel port services?

a. INT 17
b. INT 16
c. INT 15
d. INT 8

205. In programmable interrupt controller which of the following ports is referred as a controlport?

a. 19
b. 20
c. 21
d. 22

206.

The first parallel port LPT1 has port number designated from_____.

a. 178 to 17A
b. 278 to 27A
c. 378 to 37A
d. 478 to 47A

207. ___is/are the port number(s) for parallel port.

a. 20 and 21
b. 60 to 64
c. 380
d. 378

208. The number of pins in a parallel port connector are?

a. 20
b. 25
c. 30
d. 35

209. The offset address of an interrupt n will be at

a. n
b. nx2
c. nx3
d. nx4

210.

Programmable interrupt controller has two ports (20 and 21). Port 20 is the control port whileport 21 is.

a. The interrupt mask register
b. Interrupt port
c. Output port
d. Input port

211. Which of the following instruction selects memory address space?

a. MOV

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b. DEC
c. IN
d. ADD

212. The thread registration code initializes the PCB and adds it to the linked list so that the _____will give it a turn.

a. Assembler
b. Scheduler
c. Linker
d. Debugger

213. Which of the following instruction is used for disabling all the interrupts during a programexecution?

a. Cli
b. Sti
c. Reti
d. Iret

214. IBM AT has how many PICs (Programmable interrupt controller)

a. 1
b. 2
c. 3
d. 4

215. Programmable interrupt controller (PIC) has

a. One input signals and eight output signals
b. One input signal and one output signal
c. Eight input signals and one output signals
d. Eight input signals and eight output signals

216. In 8088 processor, there can be total possible entries in an interrupt vector table.

a. 256
b. 64
c. 128
d. 512

217. Each thread can have their own

a. Execution area
b. Stack
c. Memory
d. Array

218. The parallel port connector is called?

a. DB-25
b. BD-25
c. DB-24
d. BD-24

219.

In 8088, the physical memory address for Interrupt Vector Table is fixed and the tableoccupies exactly_____of memory.

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a. 1 KB
b. 1 MB
c. 64 KB
d. 4 Bytes

220. Which of the following interrupts is non maskable interrupt?

a. INT 0
b. INT 1
c. INT 2
d. INT 3

222.

Using OUT instruction on parallel data port results into a signal of_____for every 1 bit.

e. 0 V
f. 1 V
g. 5 V
h. 10 V

221. PCB stands for?

a. Process Control Block
b. Process Cleaning Block
c. Programmable Counter Block
d. Programs Control Block

222. Which of the following interrupts is used in debugging with the trap flag?

a. INT 0
b. INT 1
c. INT 2
d. INT 3

223. Which of the following port number is used to send an end of interrupt (EOI) signal to thePIC after an interrupt is ended?

a. 0x16
b. 0x20
c. 0x60
d. 0x378

224. Which of the following instruction is used for reading a char from keyboard?

a. out al, 0x60
b. in al , 0x60
c. out dx, al
d. out dx, 0x378

225. In PIC, which of the following port is used for selectively enabling or disabling interrupts?

a. 19
b. 20
c. 21
d. 22

226. DOS has single entry point to access all its services through interrupt_

a. INT 21
b. INT 16

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c. INT 2
d. INT 10

227. Which of the following pins of parallel port are used to carry the data from processor to thedevice connected through parallel port?

a. Pin 2 to pin 9
b. Pin 10
c. Pin 4
d. Pin 18 to pin 25

228. The input frequency of the programmable interval timer(PIT) is

a. Fixed
b. Depends on processor block
c. Variable
d. Depends on hardware attached

229.
All registers of task are stored in space called _____

a. Process storage block
b. Process control block
c. Process terminal
d. Swapping space

230. Which of the following IRQ`s is derived by a key board

a. IRQ 0
b. IRQ 1
c. IRQ 2
d. IRQ 3

231. The programmable interval timer (PIT) has input frequency of

a. 1.193MHz
b. 2.193MHz
c. 3.193MHz
d. 4.193MHz

232. Changing and restoring the state of Control processing unit (CPU) is referred as

a. Thrashing
b. Multitasking
c. Context Switching
d. Multithreading

233. For the external hardware to generate an interrupt there are how many pins outside theprocessor?

a. 1
b. 2
c. 3
d. 4

234. Which of the following instruction selects peripheral address space?

a. MOV
b. DEC
c. IN
d. ADD

235. Which of the following is the most commonly used port with printer?

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a. Serial Port
b. USB port
c. Parallel Port
d. DVI Port

236. **_____allows us to execute one instruction at a time rather than the whole program atonce**

a. INT 0
b. INT 1
c. INT 2
d. INT 3

237. **Which of the following is used for exporting keyboard services?**

a. INT 16
b. INT 11
c. INT 12
d. INT 8

238. **TSR stands for**

a. Terminate and store routines
b. Transmits and save resources
c. Terminate and Stay resident
d. Truncate and start recursively

239. **In older computers, the port number designed for parallel pert LPT2 area.**

- 078-07A
- 178-17A
- 378-37A
- 278-27A

240. **Which of the following is the ACK pin in DB-25 Connector**

a. 10
b. 11
c. 12
d. 13

241. **Which of the following IRQs is a cascading interrupt?**

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3

242.

In interrupt masking, we use_____to get control from the program without letting theprogram know about it.

a. IRQ 0
b. IRQ 1
c. IRQ 5
d. IRQ 7

243. **The output of programmable timer interval is connected to which line**

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of the PIC?

a. IRQ 0
b. IRQ 1
c. IRQ 2
d. IRQ 3

244.

In multitasking, each PCB requires_____bytes to store the values of registers.

a. 14
b. 28
c. 32
d. 16

245. Which of the following IRQs is used for sound card or network card?

a. IRQ 4
b. IRQ 5
c. IRQ 6
d. IRQ 7

246.

Programmable interrupt controller has_____ports.

a. 1
b. 2
c. 3
d. 4

247. Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACKpin?

a. Bit 1
b. Bit 2
c. Bit 3
d. Bit 4

248.

All the registers & stack are saved in

- Multitasking
- Multi-Processing
- Function call
- BIOS

249.

Which of the following is the source register in OUT instruction?

a. AL or AX
b. BL or BX
c. CL or CX
d. DL or DX

250. Which of the following is the destination register in IN instruction?

a. AL or AX
b. BL or BX

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c. CL or CX
d. DL or DX

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